

AMENDMENTS TO ABSTRACT

A device for prolonging the lifetime of a nonvolatile memory is applied to connect the connection between a host electronic machine with and a nonvolatile memory device, comprises The device includes a RAM (Random Access Memory) buffer zone, a counter, and two sets of inverters, wherein the RAM buffer zone is employed to store a unit data train temporarily; the counter will is used to count the total bits of logic "0"; and the interpolated the inverters are elaborated are used to lessen the times for reading/writing a nonvolatile memory device by inverting the unit data train based on the count (for example, when the number of logic "1"s exceeds the number of logic "0"s), and the state flag keeps track of whether an inversion has occurred or is needed checking a state flag to decide whether a logic inversion of the unit data train is needed or not so as to write lesser bits of logic "0" and thereby prolong the lifetime of the nonvolatile memory.